Advanced Bootkit Techniques on Android

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Who is Di Shen?

Who is Zhangqi Chen?

A developer on Android kernel and kernel modules

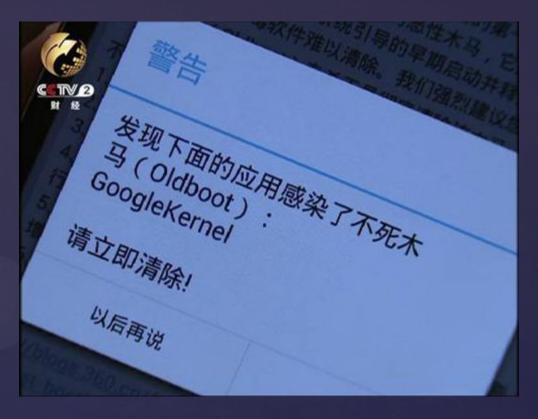
Analyzing Android kernel vulnerability and writing exploit

- ▼ To proof that the boot partition of Android could be infected easily
- ₹ Try to launch a kernel module which can run on most of Android phones
- & Exploitation of Android Kernel Rootkit

What we want to do?

- Most Phone's boot partition was infected, hard to be detected and removed
- & A kernel module, launch most phones
 - ø bypassed built-in kernel-level security restrictions
 - ø bypassed Samsung's TrustZone-based Integrity Measurement Architecture (a term of KNOX)
 - ø bypassed kernel text-code write protection on some phone's kernel such as XIAOMI
- Rootkit in kernel made all modules invisible

And the result?



Oldboot: first bootkit we found

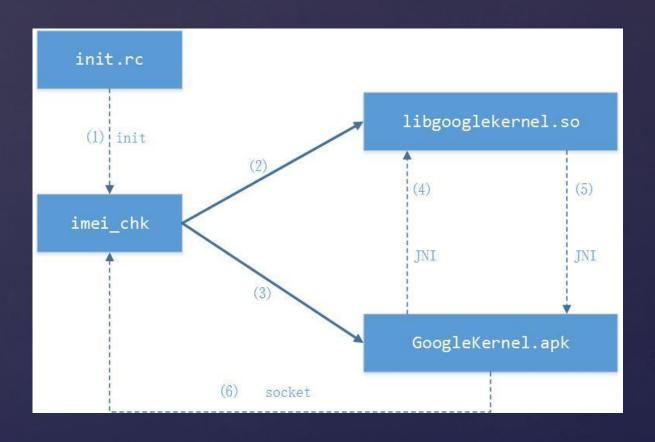
boot partition on Android

- k init in ramdisk:first process on Linux
- & Bootloader -> Kernel -> init & init.rc

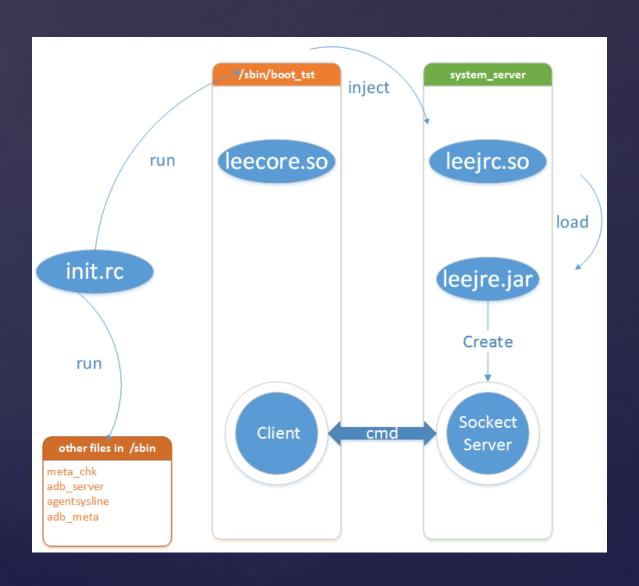
Oldboot

- ₹ First found by Qihoo,2013
- & Be pushed into custom roms' boot partition
- k the first Android-based Bootkit as we know
- Modifed booting script to launch earlier than other services of Android
- We have developed a tool to remove Oldboot: http://t.cn/8FRVFqr

Oldboot.A



Oldboot.B



several challenges of removing Oldboot

- & All modules of malware was pushed into ramdisk
 - ø an AntiVirus software without root privileges can do nothing
 - ø malware cannot be delete via filesystem operations
- - ø launch earlier than AntiVirus software
- & Easy to detect, but hard to remove
- More info:
 - ø http://t.cn/8Fb4eOC
 - ø http://t.cn/Rv5NiQo
 - ø http://blogs.360.cn/360mobile/

The future of Android Malware may...

- & not ONLY APK files can be infected
- & Anti Reverse Engineering

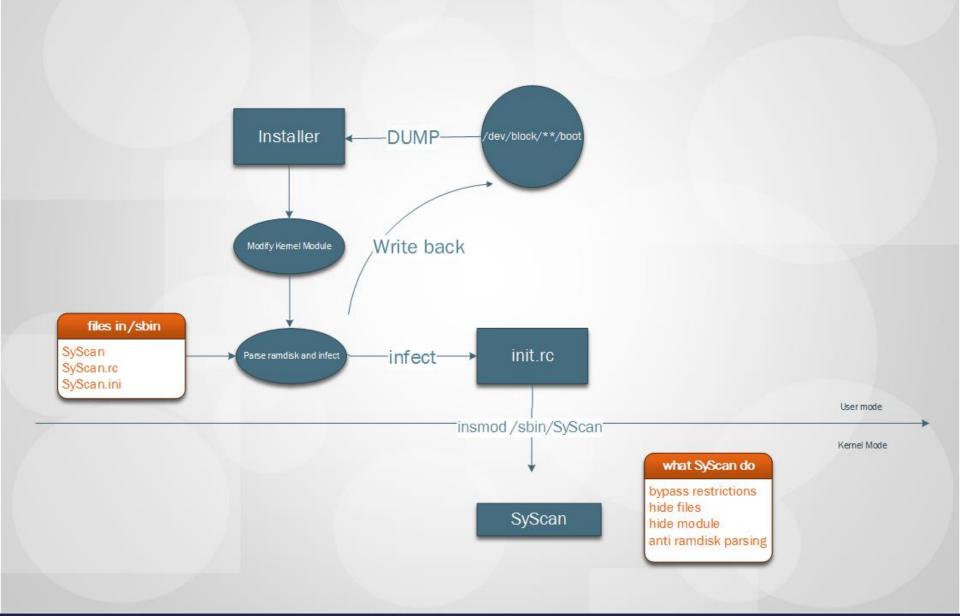
- & Self-protection mechanisms
- & Be invisible to COTS anti-virus software

Advanced bootkit attack

more advanced than Oldboot

Maybe we can make it better than Oldboot...

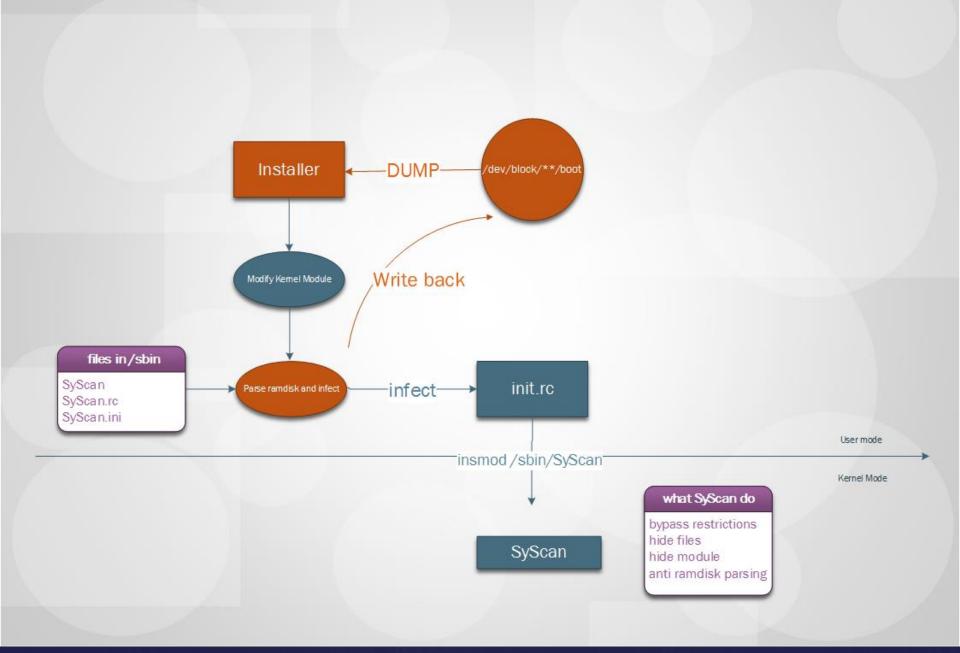
- - The malware doesn't need to be preinstalled into ROM files.
- hide itself in kernel and nobody can
 detect it from userspace



What we need to do firstly

- & Gain root privileges
 - There is still some kernel exploit can be widely used(CVE-2013-2094,CVE-2013-6282, CVE-2014-0196, CVE-2014-3153,etc)
 - Most vendors will not fixup these exploit by OTA update immediately :)
 - Bypass SE Linux restrictions
 - ø set process' context u:r:init:s0 or u:r:kernel:s0
- & We wont talk about these techniques this time

Infecting boot partitions { install the malware



install the malware into boot partition

- ₩ Write everything back to block device

Search the block device of boot

- ★ There is a magic word "ANDROID!" at the beginning of boot image header

Search the block device of boot

```
root@hwp6-u06:/dev/block/platform/hi_mci.1/by-name # ls -l
                                      2014-05-30 13:35 boot -> /dev/block/mmcblk0p12
Lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 cache -> /dev/Dlock/mmcDlkUp1/
LTWXTWXTWX TOOT
                    ΓΟΟΤ
lrwxrwxrwx root
                                      2014-05-30 13:35 cust -> /dev/block/mmcblk0p18
                    root
                                      2014-05-30 13:35 misc -> /dev/block/mmcblk0p4
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 modemimage -> /dev/block/mmcblk0p13
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 modemnvm1 -> /dev/block/mmcblk0p14
lrwxrwxrwx root
                    root
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 modemnvm2 -> /dev/block/mmcblk0p15
                                      2014-05-30 13:35 nvme -> /dev/block/mmcblk0p3
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 oeminfo -> /dev/block/mmcblk0p6
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 recovery -> /dev/block/mmcblk0p11
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 recovery2 -> /dev/block/mmcblk0p10
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 reserved1 -> /dev/block/mmcblk0p7
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 reserved2 -> /dev/block/mmcblk0p8
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 round -> /dev/block/mmcblk0p2
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 splash -> /dev/block/mmcblk0p5
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 splash2 -> /dev/block/mmcblk0p9
lrwxrwxrwx root
                    root
lrwxrwxrwx root
                                      2014-05-30 13:35 system -> /dev/block/mmcblk0p16
                    root
                                      2014-05-30 13:35 userdata -> /dev/block/mmcblk0p19
lrwxrwxrwx root
                    root
                                      2014-05-30 13:35 xloader -> /dev/block/mmcblk0p1
lrwxrwxrwx root
                    root
```

```
root@hwp6-u06:/ # busybox hexdump -C -n 200 /dev/block/mmcblk0p12
                                                              ANDROID!.FO....
0000000
         41 4e 44 52 4f 49 44 21
                                   80 46 51 00 00 80 00 00
00000010
         06 ef 0e 00 00 00 40 01
                                   00 00 00 00 00 00 f0 00
                                                              00000020
         00 01 00 00 00 08 00 00
                                   00 00 00 00 00 00 00 00
         00 00 00 00 00 00 00
                                   00 00 00 00 00 00 00 00
90000030
                                                              . . . . . . . . . . . . . . . . .
                                                             Ivmalloc=384M k3v1
00000040
         76 6d 61 6c 6c 6f 63 3d
                                   33 38 34 4d 20 6b 33 76
                                                             |2 pmem=1 mmcpart|
00000050
         32 5f 70 6d 65 6d 3d 31
                                   20 6d 6d 63 70 61 72 74
                                                             |s=mmcblk0:p1(xlo|
90000060
         73 3d 6d 6d 63 62 6c 6b
                                   30 3a 70 31 28 78 6c 6f
                                                             lader),p3(nvme),p|
00000070
         61 64 65 72 29 2c 70 33
                                  28 6e 76 6d 65 29 2c 70
                                   70 35 28 73 70 6c 61 73
                                                             [4(misc).p5(splas]
90000080
         34 28 6d 69 73 63 29 2c
                                                             |h),p6(oeminfo),p|
         68 29 2c 70 36 28 6f 65
                                   6d 69 6e 66 6f 29 2c 70
00000090
                                                             [7(reserved1).p8(]
900000a0
         37 28 72 65 73 65 72 76 65 64 31 29 2c 70 38 28
                                                             [reserved2),p9(sp]
900000Ь0
         72 65 73 65 72 76 65 64 32 29 2c 70 39 28 73 70
         6c 61 73 68 32 29 2c 70
                                                             [lash2),p[
900000€0
300000c8
```

Parse boot image header

```
52 /*
53 **
       boot header
54 **
                          1 page
55
       kernel
                         n pages
       ramdisk
58 **
                         m pages
59 *
       second stage
                         o pages
61 **
62 **
63 ** n = (kernel size + page size - 1) / page size
64 ** m = (ramdisk size + page size - 1) / page size
65**o = (second size + page size - 1) / page size
67 ** 0. all entities are page size aligned in flash
68 ** 1. kernel and ramdisk are required (size != 0)
69 ** 2. second is optional (second size == 0 -> no second)
70 ** 3. load each element (kernel, ramdisk, second) at
71** the specified physical address (kernel addr, etc)
72 ** 4. prepare tags at tag addr. kernel args[] is
73 ** appended to the kernel commandline in the tags.
74**5. r0 = 0, r1 = MACHINE TYPE, r2 = tags addr
75 ** 6. if second size != 0: jump to second addr
76 ** else: jump to kernel addr
77 */
```

Boot header

Kernel(zlmage)

Ramdisk(init.rd)

Referrence: AOSP/system/core/fastbootd/bootimg.h

boot_img_hdr

```
28 struct boot img hdr
29 {
30
      unsigned char magic[BOOT MAGIC SIZE];
31
32
      unsigned kernel size; /* size in bytes */
33
      unsigned kernel addr; /* physical load addr */
34
35
      unsigned ramdisk size; /* size in bytes */
      unsigned ramdisk addr; /* physical load addr */
36
37
38
      unsigned second size; /* size in bytes */
      unsigned second addr; /* physical load addr */
39
40
41
      unsigned tags addr; /* physical addr for kernel tags */
      unsigned page size; /* flash page size we assume */
42
43
      unsigned unused[2]; /* future expansion: should be 0 */
44
45
      unsigned char name[BOOT NAME SIZE]; /* asciiz product name */
46
47
      unsigned char cmdline[BOOT ARGS SIZE];
48
49
      unsigned id[8]; /* timestamp / checksum / shal / etc */
50 };
51
```

Referrence: AOSP/system/core/fastbootd/bootimg.h

Uncompress the ramdisk

- Ramdisk in boot.img is a gzip file gzip –d ramdisk.gz
- ∀ Finally we got all the files and directories stored in ramdisk

Many files in ramdisk are infectable

- & init.rc
- & init
- ⟨sbin/adbd⟩

Infect boot script and copy my files

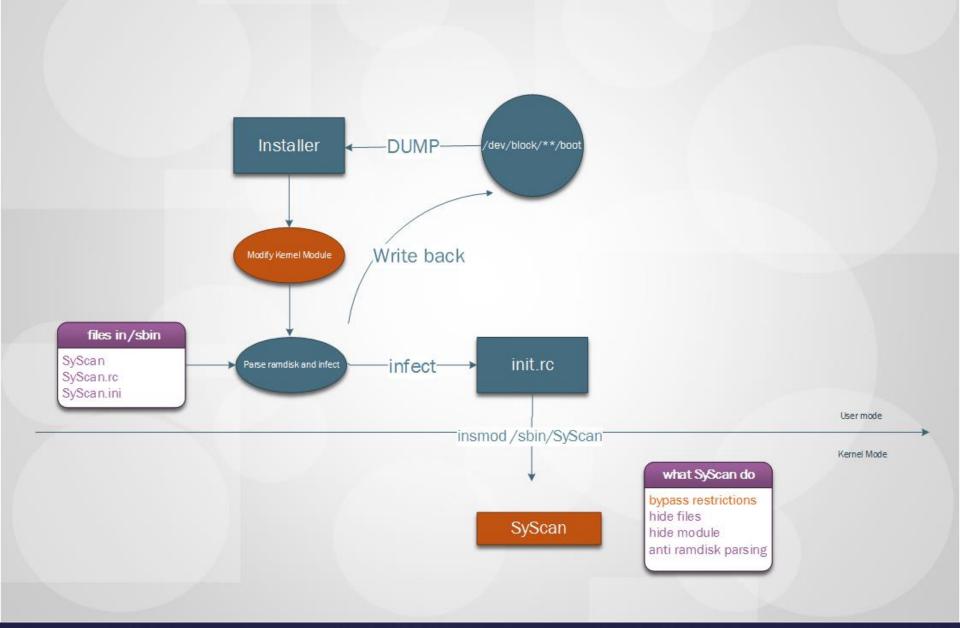
- & Add "insmod/sbin/SyScan360" to init.rc
- k Copy my files to /sbin
 - ซ SyScan360 the kernel module

Flush the infected ramdisk back

- Rebuild arguments for boot_img_hdr
- - archive files as cpio format
 (referrence:AOSP/system/core/cpio.c)
 - g Then gzip it
- Make boot header√zImage and ramdisk.gz together as boot.img
 - Referrence: AOSP/system/core/mkbootimg

Launching the kernel module

Maybe more complicated then a word "insmod"



Challenges of developing kernel module for Android

- & We can't find kernel source code for all phones
- & built-in kernel-level security restriction
- & Each version's structures may different, it is
 - hard to make our module compatible

Without devices' kernel source code

- What we need is goldfish's source code only to build our module.
- LKM(loadable kernel module) must be enabled on target device
- Make sure our "struct module" in source code big enough by adding 64 words after "struct module"

built-in kernel-level security restriction

- ∀ Vermagic check
- k module_layout(3.0) or struct_module(2.6) CRC checksum

 truct_module(2.6) CRC checksum

 truct_modu

vermagic check

```
modinfo:00000C9C
                 mod compat version42 DCB "parm=compat version:Version of the kernel compat backport work",0
.modinfo:00000CDB
                  mod compat versiontype40 DCB "parmtype=compat version:charp",0
.modinfo:00000CF9
                  mod compat base tree version38 DCB "parm=compat base tree version:The git-describe of the upstream b"
.modinfo:00000CF9
                                 DCB "ase tree",0
.modinfo:00000D42
                  mod compat base tree versiontype36 DCB "parmtype=compat base tree version:charp",0
.modinfo:00000D6A
                  mod compat base tree34 DCB "parm=compat_base_tree:The upstream tree used as base for this ba"
                                 DCB "ckport", 0
.modinfo:00000D6A
                  mod compat base treetype32 DCB "parmtype=compat base tree:charp",0
modinfo:00000DB1
.modinfo:00000DD1
                  mod compat base tree30 DCB "parm=compat base tree:The upstream verion of compat.git used",0
                  mod compat basetype28 DCB "parmtype=compat base:charp",0
modinfo:00000E0E
.modinfo:00000E29 mod license5 DCB "license=GPL",0
                  mod description4 DCB "description=Kernel compatibility module",0
.modinfo:00000E35
modinfo:00000E5D
                  _mod_author3 DCB "author=Luis R. Rodriguez",0
.modinfo:00000E76 mod license101 DCB "license=Dual BSD/GPL".0
.modinfo:00000E8B
                  mod author100 DCB "author=Broadcom Corporation",0
.modinfo:00000EA7
                  mod description99 DCB "description=Cordic functions",0
                                                                                                vermagic in .modinfo
.modinfo:00000EC4
                 mod license87 DCB "license=Dual BSD/GPL".0
                  mod author86 DCB "author=Broadcom Corporation".0
.modinfo:00000ED9
                  mod description85 DCB "description=CRC8 (by Williams, Ross N.) function",0
modinfo:00000EF5
modinfo:00000F26
                                 ALIGN 4
modinfo:00000F28
                   mod vermagic5 DCB "vermagic=3.0.8-00771-q0c49f24 SMP preempt mod unload ARMv7 p2v8
.modinfo:00000F3
.modinfo:00000F3
                                 DCB 0
.modinfo:00000F7
```

```
KUM:00505675
                                                                                          DCR
  current_fs_time
                                        ROM
                                                       ROM:C05D5676
                                                                                          DCB
f nsecs to jiffies
                                                       ROM:C05D5677
                                                                                          DCB
                                        ROM
fer_free_all(struct tty_struct * tty)
                                                       ROM: C05D5678
                                                                                          DCB
                                        ROM
                                                                                          DCB
                                                       ROM:C05D5679
f local bh disable
                                        ROM
                                                       ROM:C05D567A
                                                                                          DCB
  _local_bh_enable
                                        ROM
                                                       ROM:C05D567B
  __do_softirq
                                        ROM
                                                       ROM:C05D567C vermagic
                                                                                          DCB "3.0.64-CM-q67fbd24 SMP preempt mod unload modversions ARMv7 p2v8"
 do_softirq
                                        ROM
                                                                                               . .. . . . . . . . . . .
                                                                                          DCB
                                                       ROM:C05D567C
f local_bh_enable_ip
                                        ROM
                                                       ROM:C05D56BE
                                                                                          DCB
f local_bh_enable
                                        ROM
                                                       ROM:C05D56BF
                                                                                          DCB
f raise softirg irgoff
                                        ROM
                                                       ROM:C05D56C0 modinfo attrs
                                                                                          DCB 0x30 : 0
                                                                                                                      vermagic string in kernel
 ns_capable
                                        ROM
                                                       ROM:C05D56C1
                                                                                          DCB 0x1F
 task_ns_capable
                                        ROM
                                                       ROM:C05D56C2
                                                                                          DCB 0xA2
f capable
                                        ROM
                                                       ROM:C05D56C3
                                                                                          DCB 0xC0
 has capability noaudit
                                        ROM
                                                       ROM: C05D56C4
                                                                                          DCB 0x4C : L
f ptrace_resume
                                        ROM
                                                       ROM: C05D56C5
                                                                                          DCB 0x1F
  __ptrace_detach.part.3
                                        ROM
                                                       ROM: C05D56C6
                                                                                          DCB 0xA2 ;
                                        ROM
  __ptrace_link
```

Import function's CRC check

```
.modinfo:00000030
modinfo:00000030 ; Segment type: Pure data
modinfo:00000030
                                 AREA .modinfo, DATA, READONLY, ALIGN=0
modinfo:00000030
                                  ; ORG 0x30
                                 DCB "license=GPL".0
modinfo:000000030 aLicenseGpl
modinfo:0000003C aAuthorJamesBot DCB "author=James Bottomleu".0
modinfo:00000053 aDescriptionScs DCB "description=SCSI wait for scans".0
                                 DCB "depends=",0
modinfo:00000073 aDepends
modinfo:00000007C aVermagic3 0 31 DCB "vermagic=3.0.31-CM SMP preempt mod unload modversions ARMv7 p2
modinfo:0000007C
                                 DCB " ", 0
modinfo:0000007C : .modinfo
                                 ends
modinfo:0000007C
versions:000000CO
versions:000000C0
versions:000000C0; Segment type: Pure data
versions:000000C0
                                    AREA __versions, DATA, READONLY
versions:000000C0
                                    . UKG UXCU
versions:000000000
                                                               ← CRC
                                    DCD 0xA3F26650
versions:000000C4 aModule lawout
                                   DCB "module layout",0
                                                               ← func/struct name
versions:000000002
versions:000000D3
                                    DCB
versions:0000000D4
                                    DCB
                                           0
versions:000000D5
                                   DCB
```

```
kcrctab mod timer DCD 0xC8FD727E
kcrctab:C044DDBC
kcrctab:C044DDC0
                   kcrctab mod timer pending DCD 0x61C243FC
                   kcrctab mod timer pinned DCD 0x227BADD6
kcrctab:C044DDC4
                   kcrctab module layout DCD 0x965F803D
kcrctab:C044DDC8
                   kcrctab mount bdev DCD 0xAC7390EA
kcrctab:C044DDCC
kcrctab:C044DDD0
                   kcrctab mount nodev DCD 0xE2D90CE2
kcrctab:C044DDD4
                   kcrctab mount ns DCD 0x1620F21B
                   kcrctab mount pseudo DCD 0x36920336
kcrctab:C044DDD8
                   kcrctab mount single DCD 0x23CBD939
kcrctab:C044DDDC
                   kcrctab mount subtree DCD 0xA6E7131E
kcrctab:C044DDE0
                   kcrctab mpage readpage DCD 0xC70A1014
kcrctab:C044DDE4
                   kcrctab mpage readpages DCD 0xB9AE7080
kcrctab:C044DDE8
                   kcrctab mpage writepage DCD 0xEB3AEDC8
kcrctab:C044DDEC
```

How to bypass these restrictions

- k Kernel module's format is ELF
- № We can find some modules from target device as a referrence
- ☼ Try to find a right vermagic from referrence module and copy it to our module.
- module_layout structure's CRC value is stored from the
 beginning 64 bytes of "__versions" section,copy the value
 from referrence module to ours.
- We don't import any kernel functions to bypass other functions' CRC checking. I will find address of functions by myself while initializing.

Bypass samsung's authenticate mechanism

- k KNOX is enabled on some of Samsung devices,LKM authentication only authorizes the kernle modules that will be loaded into the kernel.(CONFIG_TIMA_LKMAUTH=y)
- Modify two instructions of function copy_and_check through /dev/kmem access technique , lkmauth will not be called any more

```
sys_init_module
load_module
copy_and_check
change instructions
to skip lkmauth
inlined lkmauth
```

Bypass samsung's authenticate mechanism

```
; CODE XREF: copy and check.isra.22+741j
1oc C00B92F4
                 MOV
                         RO, R5
                 LDR
                         R1, = 0 \times C \times OB4 \times OF9 F
                 MOV
                         R2, #4
                 BL
                         memcmp
                 CMP
                         RO, #0
                 BNE
                         1oc C00B9618
                         R3, [R5,#0x10]
                 LDRH
                 CMP
                         R3, #1
                 BNE
                         1oc C00B9618
                 MOV
                         RO, R5
                 BL
                         elf check arch
                 CMP
                         RO, #0
                 BEQ
                         1oc C00B9618
                 LDRH
                         R3, [R5,#0x2E]
                 CMP
                         R3, #0x28
                         1oc C00B9618
                 BNE
                 LDRH
                         R1, [R5,#0x30]
                 LDR
                         R2, [R5,#0x20]
                 MLA
                         R3, R3, R1, R2
                 CMP
                         R6, R3
                 BCC
                         1oc C00B9618
                         RO, =lkmauth mutex ; replace the followed two instructions to bypass lkmauth
                 LDR
                 LDR
                         R4, =module addr max
                                                 lkm_auth code start from
                 BL
                         mutex lock
                 MOV
                         R1, R6
                         RO, = 0xC 0B6 0AAB
                 LDR
                 BL
                         printk
```

Bypass samsung's authenticate mechanism

```
MOV
                       R7, #0xFFFFFFF
                                       ; CODE XREF: copy and check.isra.22+290îj
1oc C00B95F4
               LDR
                       RO, =1kmauth mutex
               BL
                       mutex unlock
                                                  ; lkm_auth end flag
               CMP
                       R7, #0
               BNE
                       loc C00B9618
set load info
                                                if authorized, change
                       R3, [SP,#0x80+var 50]
               LDR
                       R3, {R5,R6}
               STMIA
                                                info->hdr & len
                       check stack
loc_C00B9610
                                       ; CODE XREF: copy and check.isra.22+78îj
                                       ; copy and check.isra.22+841j
               MOU
                       R7, #0xFFFFFFF2
               В
                       1oc C00B961C
```

Initialization of kernel module

- Modify module structure, make init/exit can be called by kernel
- k Find address of kernel functions by kallsymbol

Modify module structure

Target phone struct module ... /* startup function. */ int (*init) (void); offset a ... /* Destruction function. */ void (*exit) (void); offset b ... }; int fill[64] };

a == A ? b == B ?

Find export function table of kernel

Search memory from 0xC0008000 with such features

Find export function table of kernel

Then you know every function address by using this call

ន Such as printk , _kmalloc

Searching sys_call_table

```
341 ENTRY (vector swi)
           adr tbl, sys call table
 399
425
            ldrcc pc, [tbl, scno, lsl #2]
176 scno
        .req r7
                                 @ syscall number
         .req r8
                                 @ syscall table pointer
177 tbl
         .req r8
                                  @ Linux syscall (!= 0)
<u>178</u> why
                                  @ current thread info
179 tsk
           .req
                 r9
```

Searching sys_call_table

exception vector table

In the case of ARM process, exception vector starts from 0xffff0000. And there is a 4 byte instruction "ldr pc, [pc, #xxx]" to branch to the software interrupt handler(vector_swi) at 0xffff0008

Then we search from vector_swi,if we get a instruction "add r8,pc,#yyy", yyy+8 is the address of sys_call_table

Searching sys_call_table

Find if from call stack

Module's init routine is called by sys_init_module; sys_init_module is called by vector_swi.

At the beginning of sys_init_module,regs are:

- R7:syscall number
- R8:address of syscall table
- R9:thread_info

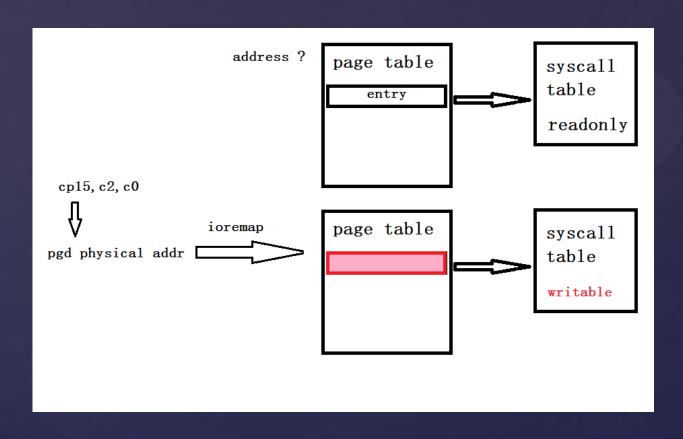
sys_init_module will push r7~r9 to stack.We can find sys_call_table by searching stack, because we always know the value of thread_info.

(thread_info = SP & 0xFFFFE000)

Hook syscall functions

- What we only need to do is modifying the value of sys_call_table[call_number]
- \Join But what if sys_call_table is READ-ONLY?
 - ø Find physical address of page table
 - ষ By coprocessor: cp15, c2, c0
 - Remap page table writable
 - Make the entry of syscall table in page table writeale
 - We find this feature on some device of XIAOMI

Make syscall table writable



Testing result

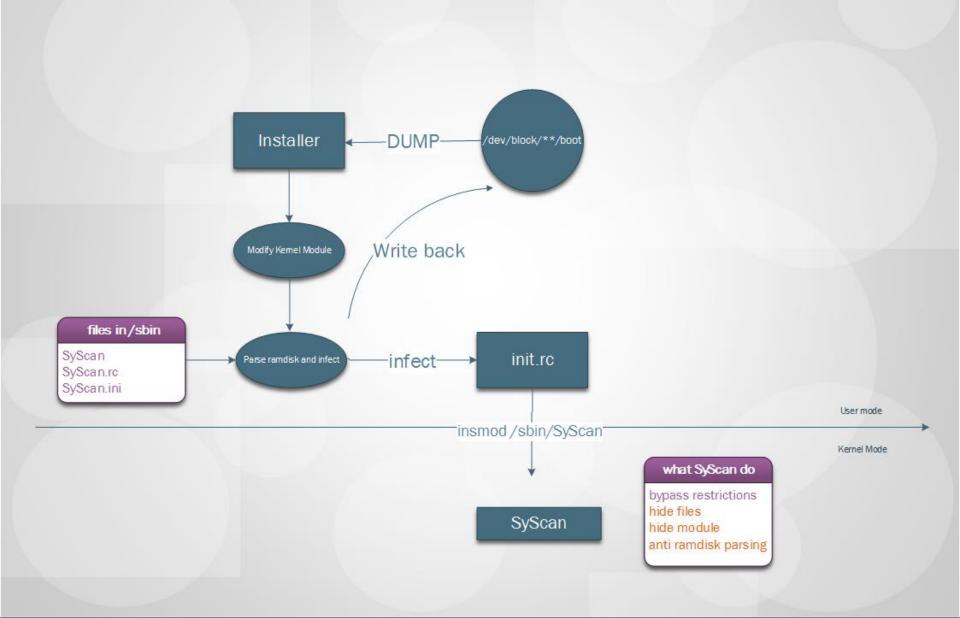
	Vendor	Mode1	CPU	cores	ARM version	kernel	RO of code	compiler	Android	Result
1	HTC	T320e	MSM8255	1	v7	3. 0. 16	no	4. 4. 3	4. 0. 3	pass
2	0PP0	x909	APQ8064	4	v7	3. 4. 0	no	4. 6. x	4. 2. 2	pass
3	Huawei	G520	MSM8x25	4	v7	3. 4. 0	no	4. 6. x	4. 1. 2	pass
4	Huawei	G510	MT6517	2	v7	3. 0. 13	no	4. 4. 3	4. 0. 4	pass
5	Huawei	G610T	MT6589M	4	v7	3. 4. 5	no	4. 6. x	4. 2. 1	pass
6	Lenovo	A798t	MT6577	1	v7	3. 0. 13	no	4.4.3	4.0	pass
7	Lenovo	A288t	SC8810	1	v7	2. 6. 35. 7	no	4. 4. 3	2. 3. 5	pass
8	Samsung	GT-N7100	Exynos 4412	4	v7	3. 0. 31	no	4. 4. 3	4. 1. 2	pass
9	Samsung	GT-I9508 (S4)	APQ8064	4	v7	3. 4. 0	no	4. 6. x	4. 2. 2	pass
10	Samsung	GT-S7562	MSM7227A	1	v7	3. 0. 8	no	4. 4. 3	4. 0. 4	pass
11	Xiaomi	1S	MSM8260	2	v7	3. 0. 8	yes	4. 4. 3	4. 0	pass
12	Xiaomi	2A	MSM8260A	2	v7	3. 4. 0	no	4. 6. x	4. 1. 1	pass
13	ZTE	V889S	MT6577	2	v7	3. 4. 0	no	4. 6. x	4. 1. 1	pass
14	ZTE	V960	MSM7227T	1	v6	2. 6. 35. 7	no	4. 4. 3	2. 3. 5	pass
15	LG	Nexus 4	APQ8064	4	v7	3. 4. 0	no	4. 6. x	4. 2. 2	fail

A series of hiding tricks

I will be invisible

Hide the bootkit

- ∀ Hide kernel module
- ∀ Hide the infected init.rc



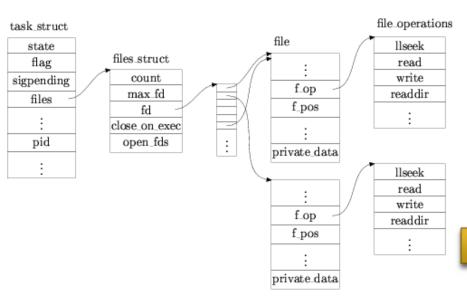
Hide kernel module

- & Direct kernel object manipulation
- ★ __this_module is module's kernel obect
- Remove __this_module from global list "modules"
- & Be invisible to Ismod command
- k Rmmod can't unload the module

Hide the infected init.rc

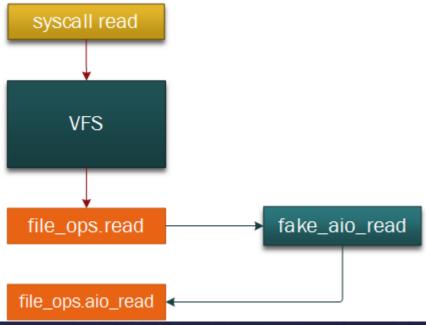
- - ø Hooking syscall table
 - ø Hooking VFS

Hooking VFS



There's a opration pointer table in every file object.

Modifying the table, every root filesystem access will be tracked.



Can VFS hooking hide everything?

- & But we cannot stop others from calling mmap.
 - ## Hooking pointers in address_space_oprations may solve this problem, but it is complicated.

File relocation

- Modifying the data while others accessing files is complicated just as we talked

- k /sbin/SyScan360.rc is a backup of the original init.rc

```
202 int file reloc(struct file* protected file, char* fake file, int flags, umode t mode){
       int ret = -1;
203
204
       int fd = 0;
205
       struct file *f = my filp open(fake file, flags, mode);
206
207
       if(!IS ERR OR NULL(f) && !IS ERR OR NULL(protected file)){
208
209
                         f->f dentry = protected file->f dentry;
210
211
                        fd = my \ qet \ unused \ fd();
212
                         my fd install(fd, f);
                         return fd;
214
215
216
       return ret;
217 }
```

Hide files in /sbin

- ★ Kernel module, backup of init.rc, and config files are in /sbin.
- ∀ Hide all of them by hooking readdir routine of VFS

Hide the data read through block device access

- We have hidden all files and module information
- - g Just like what we did to infect boot partition
 - ø dd if=/dev/block/** of=outdir
- We relocate this kind of access by the same way.
 - The original boot.img will be hidden in /data,we relocate the access by hooking syscall open and openat.

Defending and detecting Android bootkit

Let's talk about defence

Trust boot

- & Only bootloader can do this
 - ø boot image authentication by Qualcomm LK
 - ø verify_signed_bootimg in aboot
- - ø Kernel can verify /system partition
 - g But cannot verify itself while start-up

Anti Rootkit Module

- Build-in kernel module to detect malware

 Must launch erlier than malware
- № Make a restrition on block device aceess

Disable LKM

- & Loadable Kernel Module

- Nexus and some of Samsung's devices has disabled LKM after Android 4.3

Fix up vulnerability

- ₩ Without exploit, bootkit can do nothing.
- ∀ Vendors should at least fix up kernel's vulnerability, and push OTA update frequently.

DEMO

Q8A

THANK YOU!